# Asad Ullah Hil Gulib


**CONTACT INFORMATION**

Work: Teaching Assistant
Room: EE333
Computational Science Department.
University of Texas at El Paso, Texas, USA.

Home: 214 W. California Ave., El Paso, TX-79902.

Cell: +1 9152527877.

Email: agulib@miners.utep.edu


**ACADEMIC PROFILE**

- Ph.D in Computational Science (Enrolled).
  University of Texas at El Paso.
  CGPA- 4.0/4.0 .
- B. Sc. In Electrical & Electronic Engineering
  Ahsanullah University of Science & Technology.
  CGPA- 3.715/4.0.

**AWARD & ACHIEVEMENT**

- Obtained Tuition Waiver several times for excellent academic result at Ahsanullah University of Science & Technology.
- Primary School Scholarship.

**EXPERIENCE**

**Research:**

- **Quantum Mechanical Modeling of DG MOSFETs**
  Conducted Quantum Ballistic simulation of nanoscale DG MOSFETs where non-equilibrium Green's function (NEGF) formalism was used as a means to solve the Schrödinger equation in transport direction. The objective was to analyze and improve the performance of such device by stacking it with high-k material.

**Teaching:**

- **Teaching Assistant** ( September 2014 – Present)
  University of Texas at El Paso
- **Laboratory Instructor** (October 2010 – August 2014)
  North South University, Dhaka
  Department of Electrical Engineering and Computer Science (EECS)
- **Lecturer (part-time)** (May 2011 – May 2012)
  Ahasanullah University of Science & Technology, Dhaka
  Electrical & Electronic Engineering Department (EEE)

**PUBLICATIONS**

**Journal:**

- Md. Imtiaz Alamgir, **Asad Ullah Hil Gulib**, Kazi Main Uddin Ahmed, ***Performance Analysis of DG MOSFETs with High-k Stack on Top & Bottom Gate***. International Journal of Scientific Technology & Research, Vol. 1 Issue. 5, June 2012 . *http://www.ijstr.org/final-print/june2012/Performance-Analysis-of-Dg-Mosfets-With-High-K-Stack-On-Top-&-Bottom-Gate.pdf*

**Book:**

- Md. Imtiaz Alamgir, **Asad Ullah Hil Gulib**, Kazi Main Uddin Ahmed, ***Quantum Ballistic Simulation of Nanoscale Double Gate MOSFET, Performance Improvement Using High-k Gate Stack***, LAMBERT Academic Publishing, *2012. https://www.lap-publishing.com/catalog/details/store/gb/book/978-3-659-28045-0/quantum-ballistic-simulation-of-nanoscale-double-gate-mosfet?search=quantum %20ballistic*

**Professional Course**

- **Cisco Certified Network Associate (CCNA) at CISCO Networking Academy, AIUB, Bangladesh.**
  Course Coverage
  - Network Fundamentals.
  - Routing Protocols & Concepts.
  - LAN Switching & Wireless.
  - Accessing the WAN.

**Skills**

**Technical:**

- Programming Language: C/C++ , MATLAB
- Semiconductor Device Simulator: nanoMOS 4.0
- Electronic Circuit Simulator: PSPICE, Logisim, Multisim.
- Network Simulator: Packet Tracer

**Computer:**

- Adobe Photoshop, Microsoft Office-2010/2007 (Word, Excel, Power point), Windows.